	•		₹ r	· · ·		ATTY DOCKET	NO. XA-9538		SERIAL NO.	010		
	INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					Naoki YADA et al.				8. 8.90		
						EUING			GROUP			
P A 1 35, 13	3 12 8 4 2 C	,,,,	A secondary		स्तर कर्ने विकेष के किस्से के किस किस के किस के स्वास्त्र करने किस के किस क			0 S S				
	U.S. PATENI DOCUMENTS											
	*EXAMINER INITIAL		DOCUMENT NUMBER	DATE		NAME	·	CLASS	SUBCLASS	FILING DATE IF APPROPRI		
				•								
									100			
										<u> </u>		
					ļ				 			
					ļ		 					
~**\$OA	₩				Salage Carte							
				<u> </u>	ļ			<u> </u>				
					 		<u>=</u>	<u> </u>				
					-			 		-		
					<u> </u>			ļ				
	FOREIGN PATENT DOCUMENTS											
			DOCUMENT NUMBER	DATE	COUNTRY			CLASS	SUBCLASS	TRANSLATION YES NO		
	10	ВА	11-296392	10/29/1999	Japan							
	-4	ВВ		02/12/1001		· · · · · · · · · · · · · · · · · · ·				Abstrac	<u> </u>	
	DU		3-057048	03/12/1991	Japan			· -		hstrac	±	
1.61.3	D4	вс	7-210215	08/11/1995	Japan			<u> </u>		Abstrac	t	
			·									
	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)											
	Ikehashi et al., "A 60ns Access 32kByte 3-Transistor Flash for Low Power Embedded Applications," 2000 Symposium on VLSI Circuits Digest of Technical Papers, pp 162-165.											
	DG	DG CA VEST CIrcuits Digest of Technical Papers, pp 102-103.										
		-										
. V 9												
				/								
	EXAMINE	R	X Dande		DATE CONSIDERED 6/1/2004							
	*EXAMINI	ER: Init d. Incl	ial if reference considered, when	her or not citation i	is in confor	mance with MPE	EP 609; Drav	v line through	citation if not in	conformance	and not	

Form PTO-A820 (also form PTO-1449) P09C/REV03

Patent and Trademark Office * U.S. DEPARTMENT OF COMMERCE

PAGE 1

OF 1